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# Salvo Compiler Reference Manual – IAR Embedded Workbench for ARM



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### Introduction

This manual is intended for Salvo users who are targeting ARM ARM7TDMI single-chip microcontrollers with IAR's (<u>http://www.iar.com/</u>) Embedded Workbench for ARM development suite.

### **Related Documents**

The following Salvo documents should be used in conjunction with this manual when building Salvo applications with IAR Embedded Workbench for ARM:

Salvo User Manual

### **Example Projects**

Example Salvo projects for use with IAR Embedded Workbench for ARM can be found in the:

\Pumpkin\Salvo\Example\ARM

directories of every Salvo for ARM® distribution.

### **Features**

Table 1 illustrates important features of Salvo's port to IAR Embedded Workbench for ARM.



## **Reference Manual**

Ge	eneral		
Abbreviated as	IARARM		
Available distributions	Salvo Lite, LE & Pro for ARM®		
Supported targets Header file(s)	all ARM7TDMI-based devices salvoportiararm.h		
Other target-specific file(s)salvoportiararm.s79salvohook_interrupt_IR			
salv	vocfg.h		
Compiler auto-detected?	yes <sup>1</sup>		
Include target-specific header file in salvocfg.h?	recommended		
	oraries		
Behavior of user hooks in libraries	do nothing (dummy functions)		
Located in	Lib\IARARM-v4		
Contex	t Switching		
Method	<pre>function-based via OSDispatch() &amp; OSCtxSw()</pre>		
Labels required?	no		
Size of auto variables and function parameters in tasks	total size must not exceed 65,535 8-bit bytes		
	errupts		
Interrupt latency in context switcher	0 cycles		
Interrupts in critical sections controlled via	OSDisableHook(), OSEnableHook(), OSRestoreHook(), OSSaveHook()		
Interrupt status preserved in critical sections?	optional, via appropriate user functions		
Method used in critical sections	see example user functions		
Debugging			
Source-level debugging with Pro library builds?	yes		
Compiler			
Bitfield packing support?			
printf() / %p support?	yes / yes		
va_arg() support? yes			

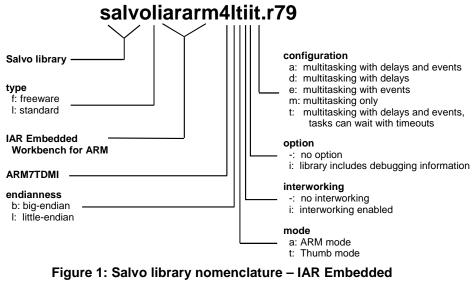
Table 1: Features of Salvo port to IAR Embedded Workbench for ARM

### Libraries

#### Nomenclature

The Salvo libraries for IAR Embedded Workbench for ARM follow the naming convention shown in Figure 1.





Workbench for ARM

Туре

Salvo Lite distributions contain *freeware* libraries. All other Salvo distributions contain *standard* libraries. See the *Libraries* chapter of the *Salvo User Manual* for more information on library types.

Target

Since the CPU instruction set is common to all target architectures based on the ARM7TDMI core, all ARM7TDMI targets use the same Salvo libraries.

**Note** There is no target-specific dependence apart from whether the target operates in little- or big-endian mode.

#### Option

Salvo Pro users can select between two sets of libraries – standard libraries, and standard libraries incorporating source-level debugging information. The latter have been built with the appropriate command-line options. This adds source-level debugging information to the libraries, making them ideal for source-level debugging and stepping in C-SPY. To use these libraries, simply select one that includes the debugging information (e.g. salvoliararm4ltiit.r79) instead of one without (e.g. salvoliararm4ltii-t.r79) in your Embedded Workbench project.



#### Configuration

Different library configurations are provided for different Salvo distributions and to enable the user to minimize the Salvo kernel's footprint. See the *Libraries* chapter of the *Salvo User Manual* for more information on library configurations.

#### **Build Settings**

Salvo's libraries for IAR Embedded Workbench for ARM are built using the default settings outlined in the *Libraries* chapter of the *Salvo User Manual*. Target-specific settings and overrides are listed in Table 2.

Compiled Limits			
Max. number of tasks	4		
Max. number of events	8		
Max. number of event flags	1		
Max. number of message queues	1		
Target-specific Settings			
Delay sizes	8 bits		
Idling hook	enabled		
Interrupt-enable bits during critical sections	controlled via user functions		
System tick counter	available, 32 bits		
Task priorities	enabled		
Watchdog timer	controlled via user functions		

Table 2: Build settings and overrides for Salvo libraries for IAR Embedded Workbench for ARM

**Note** The compiled limits for tasks, events, etc. in Salvo libraries can be overridden to be less (all Salvo distributions) or more (all Salvo distributions except Salvo Lite) than the library default. See the *Libraries* chapter of the *Salvo User Manual* for more information.

#### **Available Libraries**

Salvo Lite for ARM contains four freeware libraries in a single configuration. Salvo LE for ARM adds standard libraries in multiple configurations. Salvo Pro for ARM adds standard libraries in multiple configurations with debugging information included.

Each Salvo for ARM distribution contains the Salvo libraries of the lesser distributions beneath it. Additionally, Salvo Pro distributions contain makefiles for all possible library configurations.



### **Target-Specific Salvo Source Files**

#### salvoportiararm.s79

The source file salvoportiararm.s79 is required for Salvo Pro source-code builds.

#### **ARM vs. Thumb Mode**

By default, salvoportiararm.s79 is assembled for Thumb mode. To assemble it for ARM mode, ensure that the symbol MAKE\_FOR\_ARM is defined during assembly, e.g. from the command line or via Embedded Workbench.

#### **Big-Endian vs. Little-Endian**

salvoportiararm.s79 can be assembled for little-endian (assembler default) and big-endian (assembler command-line argument: -e) targets without any modifications.

#### Interworking

salvoportiararm.s79 can be assembled independent of any interworking settings.

### salvocfg.h Examples

Below are examples of salvocfg.h project configuration files for various different Salvo distributions and the ARM7TDMI core.

#### Salvo Lite Library Build

#define	OSUSE_LIBRARY	TRUE
#define	OSLIBRARY_TYPE	OSF
#define	OSLIBRARY_CONFIG	OST
#define	OSTASKS	3
#define	OSEVENTS	4
#define	OSEVENT_FLAGS	0
#define	OSMESSAGE_QUEUES	1

Listing 1: Example salvocfg.h for library build using salvofiararm4lti-t.r79



#### Salvo LE & Pro Library Build

#define	OSUSE_LIBRARY	TRUE
#define	OSLIBRARY_TYPE	OSL
#define	OSLIBRARY_CONFIG	OSA
#define	OSTASKS	7
#define	OSEVENTS	11
#define	OSEVENT_FLAGS	0
#define	OSMESSAGE_QUEUES	4

Listing 2: Example salvocfg.h for library build using salvoliararm4lti-a.r79 or salvoliararm4ltiia.r79

#### Salvo Pro Source-Code Build

#define #define	OSEVENTS OSEVENT_FLAGS OSMESSAGE_QUEUES OSTASKS	9 1 2 17
<pre>#define #define #define</pre>	OSENABLE_BINARY_SEMAPHORES OSENABLE_IDLING_HOOK OSENABLE_TIMEOUTS OSBYTES_OF_DELAYS OSBYTES_OF_TICKS	TRUE TRUE TRUE 1 4

Listing 3: Example salvocfg.h for source-code build

### Performance

#### **Interrupt Latencies**

Since Salvo's context switcher for IAR Embedded Workbench for ARM does not need to control interrupts, Salvo applications can easily be created with zero total interrupt latency for interrupts of interest.

In a properly-configured application, only those interrupts that call Salvo services will experience interrupt latency from Salvo's operations. Users must ensure that these interrupt sources are disabled (and re-enabled) via the user interrupt hooks.

Disabling and re-enabling interrupts globally in the user interrupt hooks (i.e., the default user interrupt hook behavior) is of course permitted, but will result in non-zero interrupt latencies for all interrupt sources, even those that do not call Salvo services. See the target-specific source files of this distribution for examples.



#### **Memory Usage**

Examples of the total memory usage of actual Salvo-based applications are listed below.

Example Application <sup>2</sup>	Program Memory Usage <sup>3</sup>	Data Memory Usage <sup>4</sup>
<pre>tut5lite (for ARM7TDMI     core)</pre>	1844	147
<pre>tut5le (for ARM7TDMI     core)</pre>	1800	147
<pre>tut5pro (for ARM7TDMI   core)</pre>	2156	143

Table 3: Program and data memory requirements for Salvo applications built with IAR Embedded Workbench for ARM

<sup>&</sup>lt;sup>1</sup> This is done automatically through the OSIAR\_ICC and \_\_TID\_\_ symbols defined by the compiler.

 $<sup>^2</sup>$  Salvo 4.0.0.

<sup>&</sup>lt;sup>3</sup> In bytes. Entire CODE section.

<sup>&</sup>lt;sup>4</sup> In bytes. Entire DATA\_Z section. This represents <u>all</u> of Salvo's objects. Does not include RAM allocated to the heap or stack. Salvo applications typically require the same (small) stack size as simple, non-multitasking applications.